

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	25	@ad<"20031121" and (clk "ck" clock\$4).ab. and ( (level\$3 clock clk "ck") near3 (shift\$3 adjust\$5) ).ab. and (processor assembl\$4 subassembl\$4).ab. and ("324" "326" "327" "716").clas.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/30 12:55
L4	2	@ad<"20031121" and (each every) with ((group \$3 subcircuit subassembl \$4 (sub adj (circuit assembl\$4))) near2 (circuit "IC" chip)) with ((clock "clk" "ck") near input)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/30 13:46
L5	136	@ad<"20031121" and ((group\$3 subcircuit subassembl\$4 (sub adj (circuit assembl\$4))) near2 (circuit "IC" chip)) with ((clock "clk" "ck") near input)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/30 13:53
L7	109	("324"/\$3.ccls. "326"/\$3.ccls. "327"/\$3.ccls. "716"/\$2.ccls.) And @ad<"20031121" And (stage with series near3 (connect\$3 interconnect \$3 (inter adj connect\$3)) near5 (power voltage potential supply source vss vcc vdd ground gnd) same (Clock\$3 "clk" "ck") )	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/30 14:41

L9	12	(US-20030179032-\$ or US-20030090292-\$ or US-20030001635-\$ or US-20020125933-\$ or US-20010028278-\$ or US-20010015665-\$ or US-20030080795-\$ or US-20030155963-\$ or US-20020063595-\$ or US-20050052220-\$).did. or (US-6208179-\$ or US- 5841300-\$).did.	US-PGPUB; USPAT	OR	ON	2010/01/30 17:06
----	----	--	--------------------	----	----	---------------------

**1 / 30 / 2010 5:20:54 PM****C:\ Documents and Settings\ SMemula\ My Documents\ EAST\ Workspaces\ 10534462b.wsp**